



VRAP-AEB-NT-HDL-7.5-million-Virtex

Product code: U013

Product Brief:

The VRAP-AEB system is the perfect solution for FPGA / ASIC validation engineers whose needs cover the entire gamut of verification.

Features:

1. Contains up to 7.5 million FPGA gates (3* 2.5 million).
2. The compilation/simulation of HDL code is independent of the density of chip. Hence emulation is multiple times faster.
3. Can view behavioral and emulation results at the same time.
4. Auto-comparison of behavioral and emulation results.
5. Support for VHDL and VerilogHDL.
6. Comes with VRAP PCI interface card from VDesign.
7. Completely compatible with Windows NT/9x.
8. Supports all the major simulators with FLI/PLI interface.
ModelSim SE/EE, NC-VHDL*, NC-Verilog*, VCS*.

Deliverables:

1. VRAP-AEB system with 7.5 million gates.
2. Power supply adaptor.
3. VDesign PCI card with interface cable for the VRAP-AEB system.
4. VRAP-AEB-HDL 3.1 software, on CD-ROM.
5. Documents.

Notes:

1. The software, which will be provided with the hardware, will be only for the respective operating system.
1. The user's computer must have one free 32-bit 33MHz PCI slot, which is used for PCI card, supplied by VDesign.

* For further details please contact the factory.



VRAP-AEB-SUN-HDL-7.5 million Virtex

Product code: U014

Product Brief:

The VRAP-AEB system is the perfect solution for FPGA / ASIC validation engineers whose needs cover the entire gamut of verification.

Features:

1. Contains up to 7.5 million FPGA gates (3* 2.5 million).
2. The compilation/simulation of HDL code is independent of the density of chip.
Hence emulation is multiple times faster.
3. Can view behavioral and emulation results at the same time.
4. Auto-comparison of behavioral and emulation results.
5. Support for VHDL and VerilogHDL.
6. Comes with VRAP PCI interface card from VDesign.
7. Completely compatible with Sun SPARC Solaris versions 2.6, 7, 8 environments.
8. Supports all the major simulators with FLI/PLI interface.
ModelSim SE/EE, NC-VHDL*,NC-Verilog*, VCS*.

Deliverables:

1. VRAP-AEB system with 7.5 million gates.
2. Power supply adaptor.
3. VDesign PCI card with interface cable for the VRAP-AEB system.
4. VRAP-AEB-HDL 3.1 software on CD-ROM.
5. Documents.

Notes:

1. The software, which will be provided with the hardware, will be only for the respective operating system.
2. The user's computer must have one free 32-bit 33MHz PCI slot, which is used for PCI card, supplied by VDesign.

* For further details please contact the factory.



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