



- Links The Mathworks MATLAB (Signal Processing ToolBox and Filter Design Toolbox) or Simulink environment with VRAP UCB.
- Supports a variety of xilinx, Altera, actel, any devices.\*
- Enables rapid prototyping vrap boards
- Exports vectors to the vrap from simulink and imports the data into the SIMULINK to facilitate visual analysis
- Automatically generates simulink block from given a VHDL Or verilog design file.
- Automatically creates the necessary mapping to the vrap board.
- Enables cycle-accurate design simulation



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