



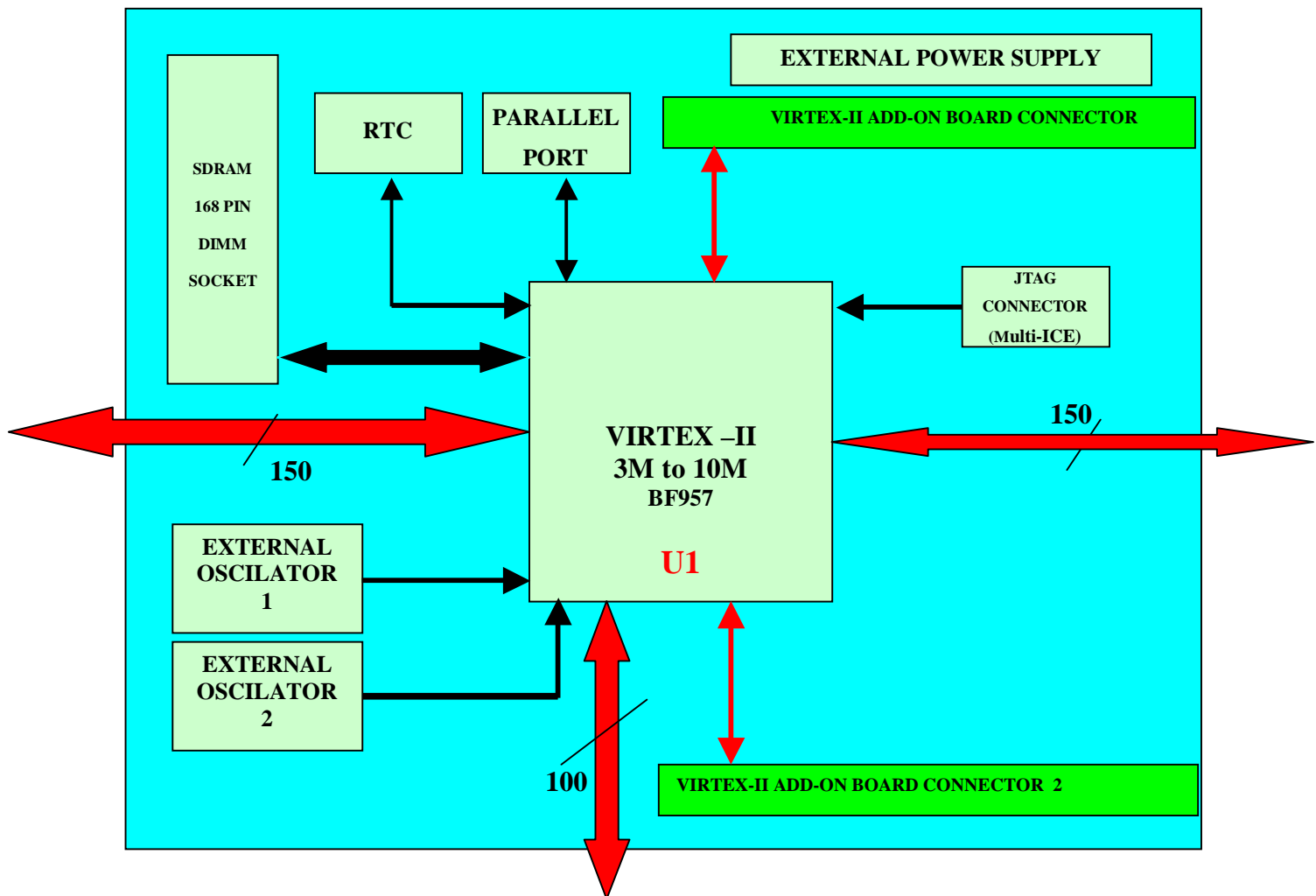
Code: VD-V2ADA

## Virtex II Advanced Adaptor

### Highlights:

- ❑ 168-pin 3.3V SDRAM DIMM socket.
- ❑ Xilinx Multi-LINX Connectors.
- ❑ Onboard Oscillators.
- ❑ External Power Supply Support.
- ❑ Inside VRAP-PEB, it is an ASIC EMULATOR, outside the system works like Single Board computer.
- ❑ Real Time Clock (RTC) with battery.
- ❑ 7 different power supplies.
- ❑ Internal Node Monitoring.

### Block Diagram:





## **Application:**

- ❑ SDRAM emulator (up to 1GB).
- ❑ SRAM/FLASH emulator (up to 1GB).
- ❑ 150 pin Pattern Generator.
- ❑ 143 IO interface for ADD ON BOARD.
- ❑ 150 pin State Analyzer.
- ❑ Single Chip User Logic Emulation up to 10Million.
- ❑ Single Chip IP Core development.
- ❑ Multimillion gate emulation (up to 10 million/adaptor).
- ❑ Independent Single Chip Controller with External Power supply.
- ❑ Complete Product Prototyping using relevant ADD ON BOARD.
- ❑ Application areas: High Speed Networking, Signal processing, Single Board Computer etc.

## **NOTE:**

- ❑ Necessary IP core for Peripheral emulation needs to be ordered separately.



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