



## **VRAP-UCB** (**VDesign Rapid Asic Prototyping – Universal Chip Board)**

### **HIGHLIGHTS**

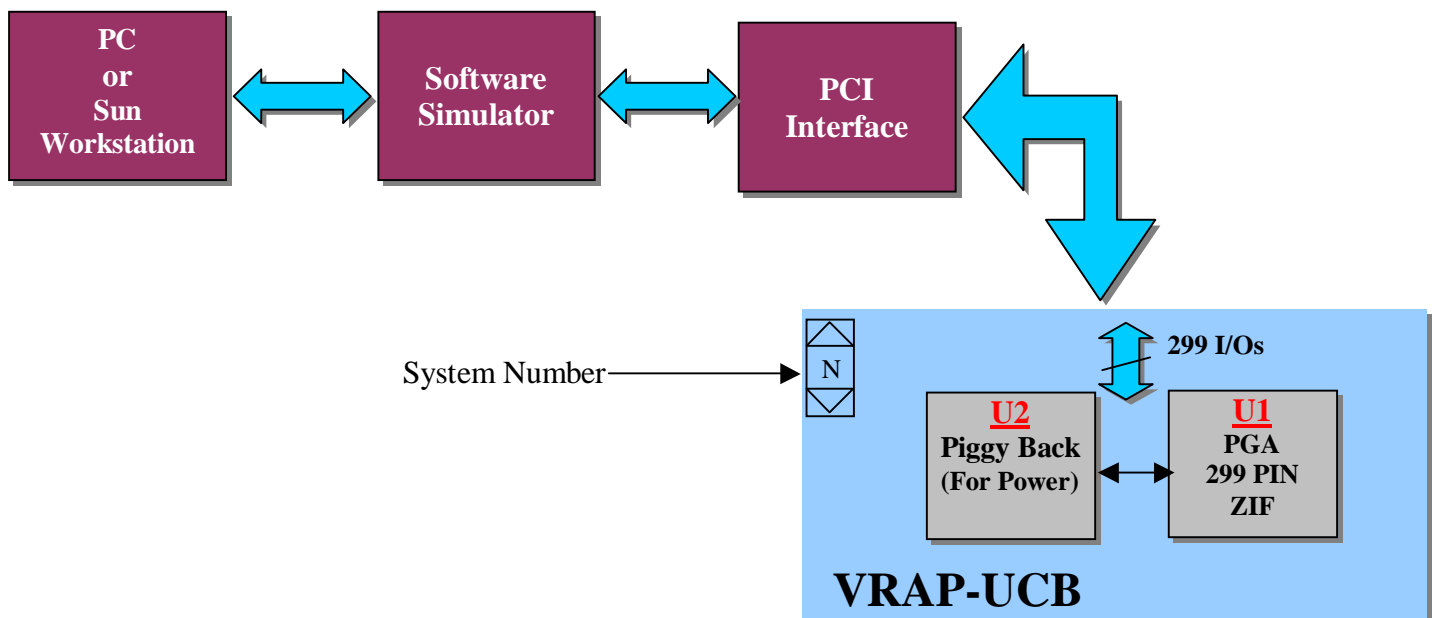
- ❑ Validate your ASIC from foundry from your simulator.
- ❑ Validate your FPGA from any vendor (XILINX, ALTERA, ACTEL, etc) from your simulator.
- ❑ Compare your ASIC/FPGA with behavioral or RTL code.
- ❑ Replace your expensive Software Simulation Model or bus functional models directly with the off the shelf available digital IC's or STANDARD PARTS or available ASIC if the IC is a CMOS STATIC CORE.
- ❑ Support for 32 Bit 33 MHz PCI 2 SLOT adaptor Plug any PCI static core Cards.
- ❑ Target your design towards XILINX or ALTERA for emulation.
- ❑ MAP your black box in VHDL or VERILOG to the VRAP-UCB board.
- ❑ Multi chip Emulation on to same or different target technology using more than one VRAP-UCB.
- ❑ Compare your design with "Standard Part" available in the market, if the standard part is a CMOS STATIC CORE.
- ❑ Integrate your design with multi chip or multi targeted FPGA/ASIC technology.
- ❑ Boon for hardware and software core IP developers and users.
- ❑ Save your simulation time by plugging FLASH/SRAM/SDRAM (up to 1GB) emulator.
- ❑ Gate level Simulation Acceleration.
- ❑ Application Board Validation using right adaptors (option).



## FEATURES

- ❑ Supports ASIC with pin count maximum 299 pins.
- ❑ Supports all major FPGA from major FPGA vendors.
- ❑ Maximum of 299 I/O's.
- ❑ Compilation is more than 1000 times faster.
- ❑ Simulation is more than 1000 times faster.
- ❑ Can mix behavioral code and VRAP-UCB board in the same design.
- ❑ Can compare between behavioral simulation and VRAP-UCB.
- ❑ Internal node monitoring is possible.
- ❑ Multiple VRAP' s can be cascaded.
- ❑ Comes with VRAP PCI interface card from VDesign.
- ❑ Works with PC/Windows and Sun Sparc Workstation (Solaris).

## BLOCK DIAGRAM :





**VRAP – UCB Ordering Information**

**CODE :** VDVRAP – U – 3v3/5v – PC/WS – M/T – S/C – VH/VE/VV – HU01/02

**Voltage Support :**  
 3v3 : 3.3v,3.3v,2.5v/1.8v  
 5v : 5v,3.3v,2.5v/1.8v

**Operating System Support :**  
 PC : WIN9x ,NT.  
 WS :Workstation .  
 ( Sun Solaris 2.6,7 and 8 only)

**Simulator Support :**  
 M : Model SIM SE/EE.  
 T : Other Simulators\*

**Unit Support :**  
 S : Singel Unit  
 C : Cascadeable Unit

**HDL Support :**  
 VH : VHDL only.  
 VE : Verilog only.  
 VV : Verilog,VHDL.

**HUB Support :**  
 HU01 : Without HUB.  
 HU02 : HUBs.

\*Contact Factory

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Release Version: Ver1.0, August 2002.