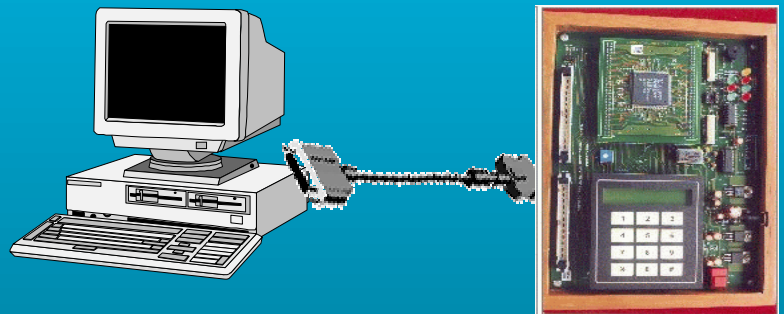


Advanced FPGA Trainer System

FPGA Prototyping Board



SIMULATE

SYNTHESISE

Place & Route

Download & Validate

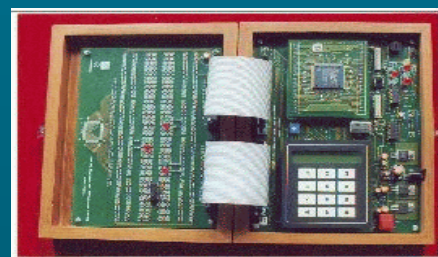
VDesign

U50K Technical Specifications

- Different technology support(Altera, Actel, Xilinx,etc).
- XILINX (SPARTAN II 50K gate density).
- ALTERA (ACEX1K Device 50K gate density).
- External Frequency Oscillator 16Mhz in system, up gradable up to 80Mhz to 150 MHz.
- Language Support
(VHDL, Verilog HDL, Mixed HDL)
- Different Voltage support
(5 Volt, 3.3 Volt, 2.5 Volt or 1.8Volt)
- Simulator support (**ModelSIM**)
- Synthesis support (**Leonardo Spectrum**)
- Place &Route support
(**XILINX Foundation and Alliance series**
ALTERA QUARTUS and MAxplus2)
- Prototyping board with support for 100pin QFP (optional) CPLD support, it can be linked with any DSP kit.
- 41 external I/O s to control external devices or another FPGA suitable for Custom
- UPLINK / DOWNLINK Possible with PC uses RS232 C (optional)

It supports only one technology at a time

VDesign



PROJECTS LIST

1. **Digital Alarm Clock**
2. **Embedded Controller**
3. **Frequency Synthesizer**
4. **Transmitter & Receiver**

EXERCISES LIST

1. **BCD Counter**
2. **Binary Counter**
3. **Gray Counter**
4. **Rolling Display**
5. **Static Display**
6. **Keypad Macro**
7. **FIFO**
8. **LIFO**
9. **Up \ Down Counter(with & without control)**
10. **Calculator(addition, subtraction)**
11. **Calculator(multiplication, division)**
12. **Random number**
13. **Frequency Divider**

OTHER PRODUCTS WHICH WORKS WITH U50K

- **BOUNDARY SCAN TOOL**
- **VRAP AEB UNIVERSITY MODEL**
- **VRAP UCB UNIVERSITY MODEL**
- **ADD ON MEMORY BOARDS**
- **ADD ON APPLICATION BOARDS**

VDesign

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