

**IPVALIDATE-NT-Xilinx-VirtexII-6 million**

**Product code: U021**

***Product Brief:***

IP Validate is the numero-uno verification tool from VDesign. Now you can run your designs at near real-time speeds. Add interface to your design. Capture node at real time.

***Features:***

1. Complete functional verification of your design.
2. Faster debugging with the Internal Node Monitoring tool with trace up to 1GB memory.
3. The compilation/simulation of HDL code is independent of the density of chip. Hence emulation is multiple times faster.
4. Contains VirtexII 6-million gate density FPGA.
5. Can view behavioral and emulation results at the same time (static emulation).
6. Auto-comparison of behavioral and emulation results (static emulation).
7. Emulate your designs at near real-time speeds.
8. Support for VHDL and VerilogHDL.
9. Comes with VRAP PCI interface card from VDesign.
10. Completely compatible with Windows NT.
11. Supports all the major simulators with FLI/PLI interface.  
ModelSim SE/EE, NC-VHDL\*,NC-Verilog\*,VCS\*.

***Deliverables:***

1. IP Validate system.
2. Power supply adaptor.
3. VDesign PCI card with interface cable for the IP Validate system.
4. IP Validate 3.1 software on CD-ROM.
5. Documents.

***Notes:***

1. The software, which will be provided with the hardware, will be only for the respective operating system.
2. The user's computer must have one free 32-bit 33MHz PCI slot, which is used for PCI card, supplied by VDesign.

\* For further details please contact the factory.

**IP VALIDATE-SUN-VirtexII-6 million****Product code: U022*****Product Brief:***

IP Validate is the numero-uno verification tool from VDesign. Now you can run your designs at near real-time speeds. Add interface to your design. Capture node at real time.

***Features:***

1. Complete functional verification of your design.
2. Faster debugging the Internal Node Monitoring tool with trace up to 1GB memory.
3. The compilation/simulation of HDL code is independent of the density of chip. Hence emulation is multiple times faster.
4. Contains VirtexII 6-million gate density FPGA.
5. Can view behavioral and emulation results at the same time (static emulation).
6. Auto-comparison of behavioral and emulation results (static emulation).
7. Emulate your design at near real-time speeds.
8. Support for VHDL and VerilogHDL.
9. Comes with VRAP PCI interface card from VDesign.
10. Completely compatible with Sun SPARC Solaris 2.6, 7, 8 operating systems.
11. Supports all the major simulators with FLI/PLI interface.  
ModelSim SE/EE, NC-VHDL\*,NC-Verilog\*,VCS\*.

***Deliverables:***

1. IP Validate system.
2. Power supply adaptor.
3. VDesign PCI card with interface cable for the IP Validate system.
4. IP Validate 3.1 software, on CD-ROM.
5. Documents.

***Notes:***

1. The software, which will be provided with the hardware, will be only for the respective operating system.
2. The user's computer must have one free 32-bit 33MHz PCI slot, which is used for PCI card, supplied by VDesign.

\* For further details please contact the factory.

**IPVALIDATE-NT-VirtexII-8 million****Product code: U023*****Product Brief:***

IP Validate is the numero-uno verification tool from VDesign. Now you can run your designs at near real-time speeds. Add interface to your design. Capture node at real time.

***Features:***

1. Complete functional verification of your design.
2. Faster debugging the Internal Node Monitoring tool with trace up to 1GB memory.
3. The compilation/simulation of HDL code is independent of the density of chip. Hence emulation is multiple times faster.
4. Contains VirtexII 8-million gate density FPGA.
5. Can view behavioral and emulation results at the same time (static emulation).
6. Auto-comparison of behavioral and emulation results (static emulation).
7. Emulate your designs at near real-time speeds.
8. Support for VHDL and VerilogHDL.
9. Comes with VRAP PCI interface card from VDesign.
10. Completely compatible with Windows NT.
11. Supports all the major simulators with FLI/PLI interface.  
ModelSim SE/EE, NC-VHDL\*,NC-Verilog\*,VCS\*.

***Deliverables:***

1. IP Validate system.
2. Power supply adaptor.
3. VDesign PCI card with interface cable for the IP Validate system.
4. IP Validate 3.1 software on CD-ROM.
5. Documents.

***Notes:***

1. The software, which will be provided with the hardware, will be only for the respective operating system.
2. The user's computer must have one free 32-bit 33MHz PCI slot, which is used for PCI card, supplied by VDesign.

\* For further details please contact the factory.

**IP VALIDATE-SUN-VirtexII-8 million**

**Product code: U024**

***Product Brief:***

IP Validate is the numero-uno verification tool from VDesign. Now you can run your designs at near real-time speeds. Add interface to your design. Capture node at real time.

***Features:***

1. Complete functional verification of your design.
2. Faster debugging the Internal Node Monitoring tool with trace up to 1GB memory.
3. The compilation/simulation of HDL code is independent of the density of chip. Hence emulation is multiple times faster.
4. Contains VirtexII 8-million gate density FPGA.
5. Can view behavioral and emulation results at the same time (static emulation).
6. Auto-comparison of behavioral and emulation results (static emulation).
7. Emulate your design at near real-time speeds.
8. Support for VHDL and VerilogHDL.
9. Comes with VRAP PCI interface card from VDesign.
10. Completely compatible with Sun SPARC Solaris 2.6, 7, 8 operating systems.
11. Supports all the major simulators with FLI/PLI interface.  
ModelSim SE/EE, NC-VHDL\*,NC-Verilog\*,VCS\*.

***Deliverables:***

1. IP Validate system.
2. Power supply adaptor.
3. VDesign PCI card with interface cable for the IP Validate system.
4. IP Validate 3.1 software, on CD-ROM.
5. Documents.

***Notes:***

1. The software, which will be provided with the hardware, will be only for the respective operating system.
2. The user's computer must have one free 32-bit 33MHz PCI slot, which is used for PCI card, supplied by VDesign.

\* For further details please contact the factory.



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